

| | Type | L # | Hits | Search Text | DBs | Time Stamp | Comments |
|----|------|-----|------|---|--|----------------------|----------|
| 50 | BRS | L50 | 80 | (plural\$3 near3 gain\$1 or multi\$3 near3 gain\$1 or Plural\$3 near23 AGC\$1 or multi\$3 near3 AGC\$1 or series near3 GA\$1 or serial\$4 near3 gain\$1 near2 amplif\$9 or cascad\$3 near3 gain\$1 near3 amplif\$9 or plural\$3 near3 GA\$1 or multi\$3 near3 GA\$1) same (PLL\$1 or phase adj2 lock\$3 adj2 loop\$1 or DPLL\$1 or DLL\$1) | US- PGPUB ; USPAT ; EPO; JPO | 2005/03/2 5 09:51 | |
| 51 | BRS | L51 | 28 | (plural\$3 near3 adjust\$6 near3 gain\$1 or multi\$3 near3 adjusta\$6 near3 gain\$1 or series near3 adjust\$6 near3 gain\$1 or serial\$4 near3 adjust\$6 near3 gain\$1 or plural\$3 near3 loop\$1 near3 gain\$1 or multi\$3 near3 loop\$1 near3 gain\$1 or plural\$3 near3 feedback near3 gain\$1 or multi\$3 near3 feedback near2 gain\$1 or vga\$1 near3 plural\$3 or multi\$3 near3 vga\$1 or series near3 vga\$1 or serial\$3 near3 vga\$1 or cascad\$3 near3 vga\$1 or voltage adj2 gain adj1 amplif\$9 near3 multi\$3 or voltage adj2 gain\$1 adj1 amplif\$9 near3 plural\$3) same (PLL\$1 or phase adj2 lock\$3 adj2 loop\$1 or DPLL\$1 | US- PGPUB ; USPAT ; EPO; JPO | 2005/03/2 5 10:18 | |

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|--|--|--|--|------------|--|--|--|
| | | | | or DLL\$1) | | | |
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| | Error Definition | Err ors |
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| 50 | | |
| 51 | | |

| | Type | L # | Hits | Search Text | DBs | Time Stamp | Comments |
|----|------|-----|------|--|--------------------------------------|------------------|----------|
| 52 | BRS | L52 | 292 | (plural\$3 near3 buffer\$3 or multi\$3 near3 buffer\$3 or series near3 buffer\$3 or serial\$4 near3 buffer\$3) same (PLL\$1 or phase adj2 lock\$3 adj2 loop\$1 or DPLL\$1 or DLL\$1) | US-PGPUB ; USPAT ; EPO; JPO | 2005/03/25 10:21 | |
| 53 | BRS | L53 | 22 | 52 and (vga\$1 or voltage adj2 gain adj2 amplif\$9 or gain\$1 near2 amplif\$9) | US-PGPUB ; USPAT ; EPO; JPO | 2005/03/25 10:22 | |
| 54 | BRS | L54 | 352 | (plural\$3 near3 driv\$3 or multi\$3 near3 driv\$3 or series near3 driv\$3 or serial\$4 near3 driv\$3) same (PLL\$1 or phase adj2 lock\$3 adj2 loop\$1 or DPLL\$1 or DLL\$1) | US-PGPUB ; USPAT ; EPO; JPO | 2005/03/25 10:32 | |
| 55 | BRS | L55 | 22 | 53 and (vga\$1 or voltage adj2 gain adj2 amplif\$9 or gain\$1 near2 amplif\$9) | US-PGPUB ; USPAT ; EPO; JPO | 2005/03/25 10:22 | |
| 56 | BRS | L56 | 37 | 54 and (vga\$1 or voltage adj2 gain adj2 amplif\$9 or gain\$1 near2 amplif\$9) | US-PGPUB ; USPAT ; EPO; JPO | 2005/03/25 10:22 | |
| 57 | BRS | L57 | 56 | ("5666322").URPN. | USPAT | 2005/03/25 10:28 | |

| | Error Definition | Err ors |
|----|---------------------|------------|
| 52 | | |
| 53 | | |
| 54 | | |
| 55 | | |
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| 57 | | |

| | Type | L # | Hits | Search Text | DBs | Time Stamp | Comments |
|----|------|-----|------|---|--|----------------------|----------|
| 58 | BRS | L58 | 5 | ("4654864" "5440515" "5452324" "5475718" "5539344").PN. | US- PGPUB ; USPAT ; USOCR | 2005/03/2 5 10:30 | |
| 59 | BRS | L59 | 8 | 57 and (vga\$1 or va\$1 or voltage adj2 gain\$1 or voltage adj3 amplif\$4) | US- PGPUB ; USPAT ; USOCR | 2005/03/2 5 10:31 | |
| 60 | BRS | L60 | 7 | (plural\$3 near3 differntial near2 amplif\$9 or multi\$3 near3 differential\$1 near3 amplif\$9 or series near3 differential\$3 near3 amplif\$9 or serial\$4 near3 differential near3 amplif\$9) same (PLL\$1 or phase adj2 lock\$3 adj2 loop\$1 or DPLL\$1 or DLL\$1) | US- PGPUB ; USPAT ; EPO; JPO | 2005/03/2 5 10:33 | |

| | Error Definition | Err ors |
|----|---------------------|------------|
| 58 | | |
| 59 | | |
| 60 | | |